

Figure 1

FIG. 2 is a block diagram of a system 104, in accordance with the present invention. The system 104 includes a processor 202, a memory 206, a secondary storage 216, a network interface 218, and an input/output interface 220. The processor 202 is connected to the memory 206, the secondary storage 216, the network interface 218, and the input/output interface 220. The memory 206 includes an operating system 208, a registration process 210, a registrar database 212, and an audit file 214. The network interface 218 is connected to networks 112 and 116.

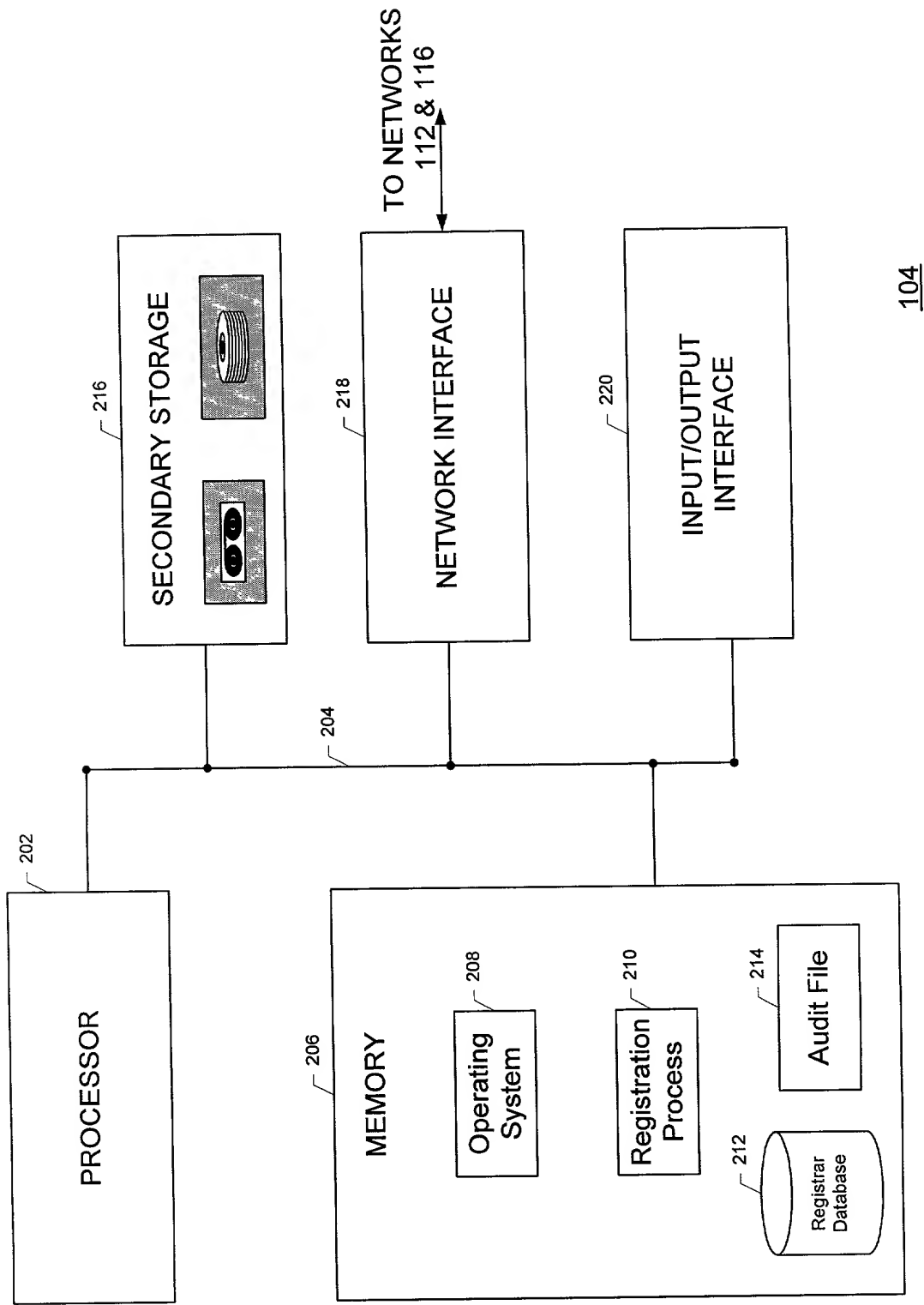


Figure 2

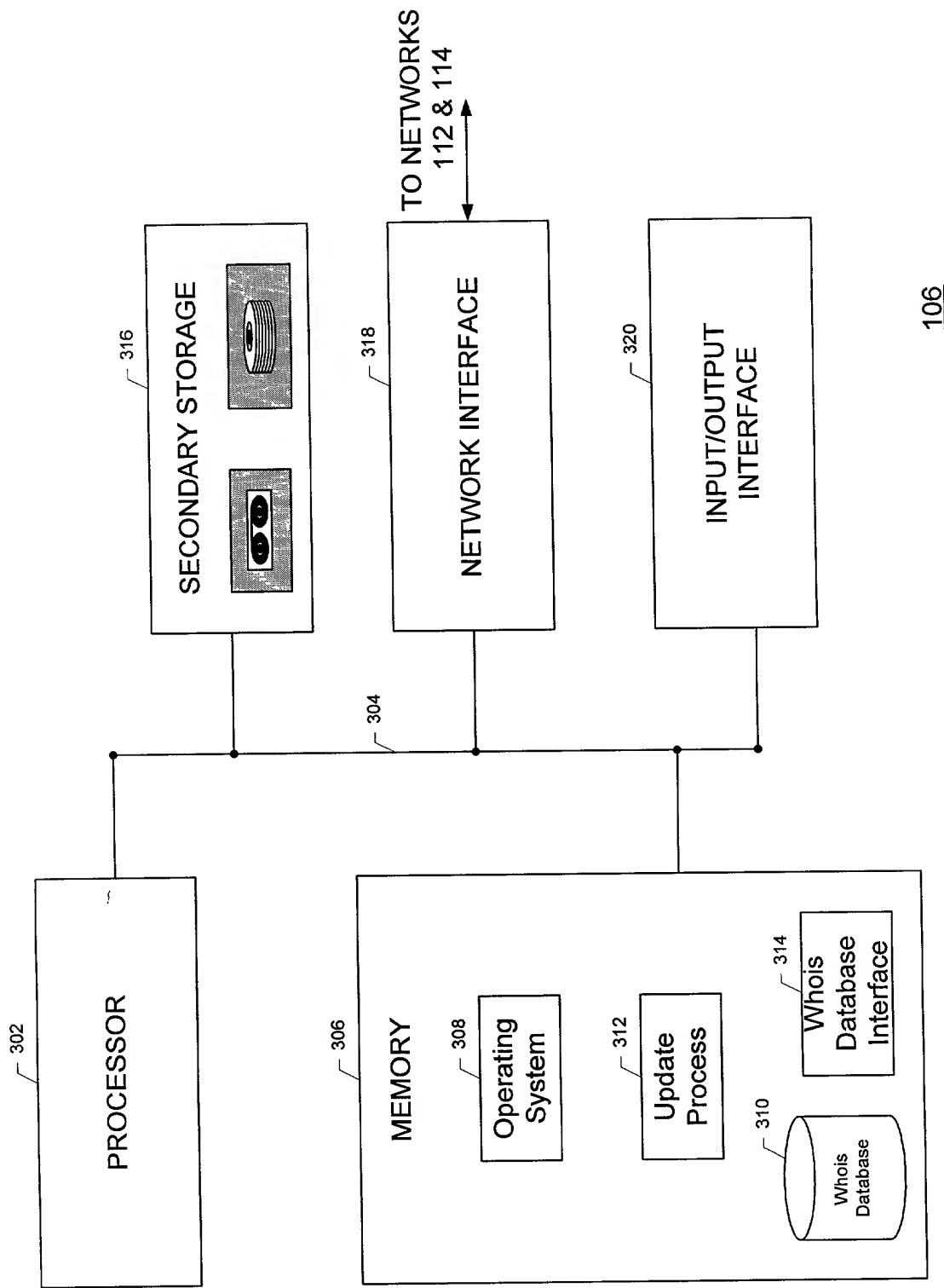


Figure 3

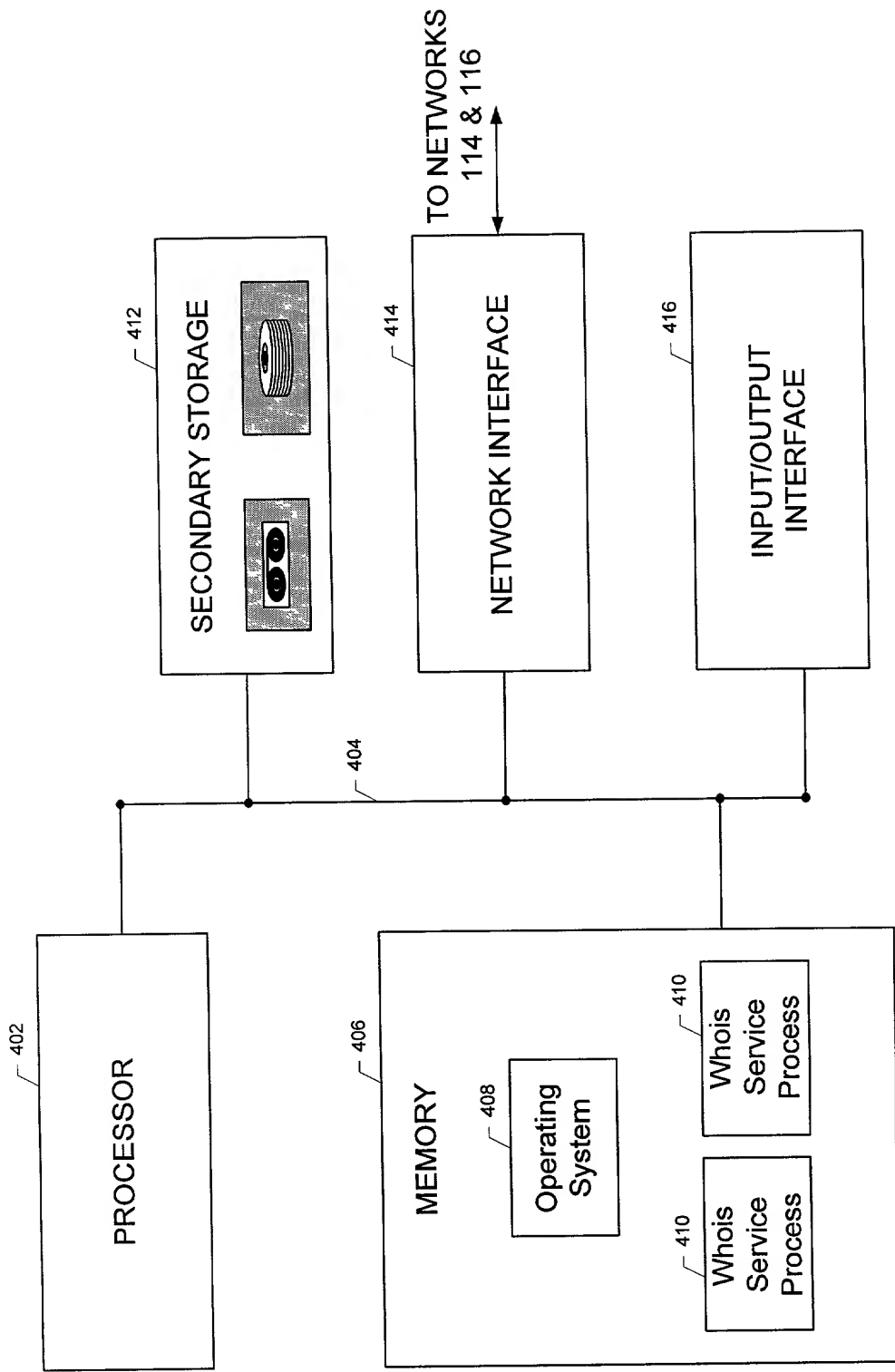


Figure 4

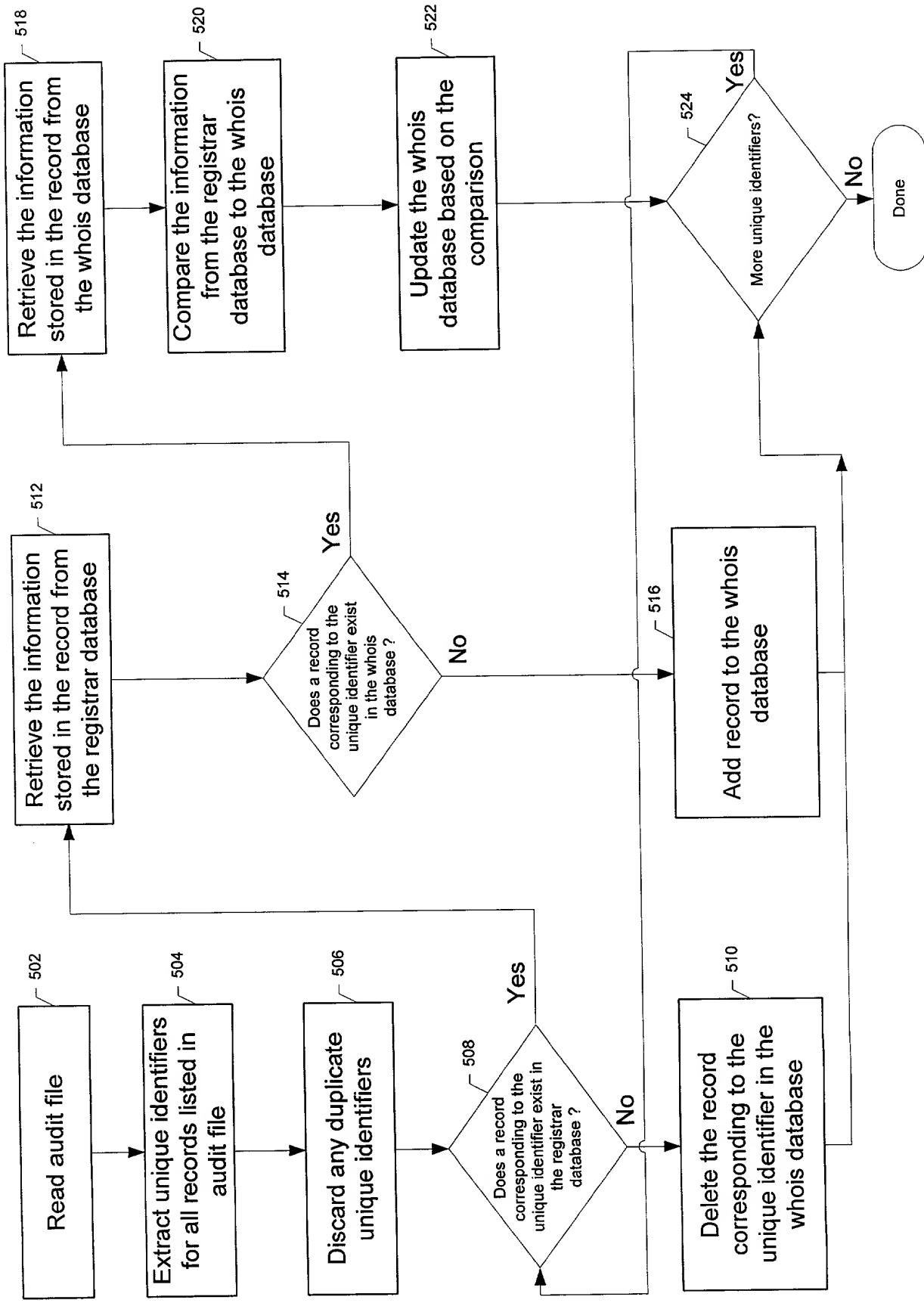


Figure 5

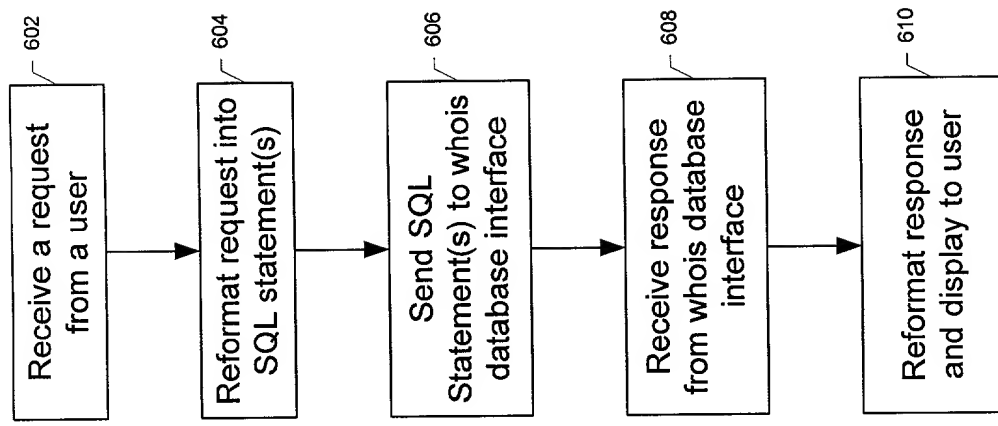


Figure 6

FIG. 7 is a block diagram of a system 700 in accordance with the present invention. The system 700 includes a processor 702, a memory 706, a secondary storage 724, a network interface 726, and an input/output interface 728. The processor 702 is connected to the memory 706, the secondary storage 724, the network interface 726, and the input/output interface 728. The memory 706 includes an operating system 708, a copy process 722, an update process 712, an index process 718, a dump process 714, index files 720, a whois database 710, and a flat file database 716. The network interface 726 is connected to networks 112 and 114.

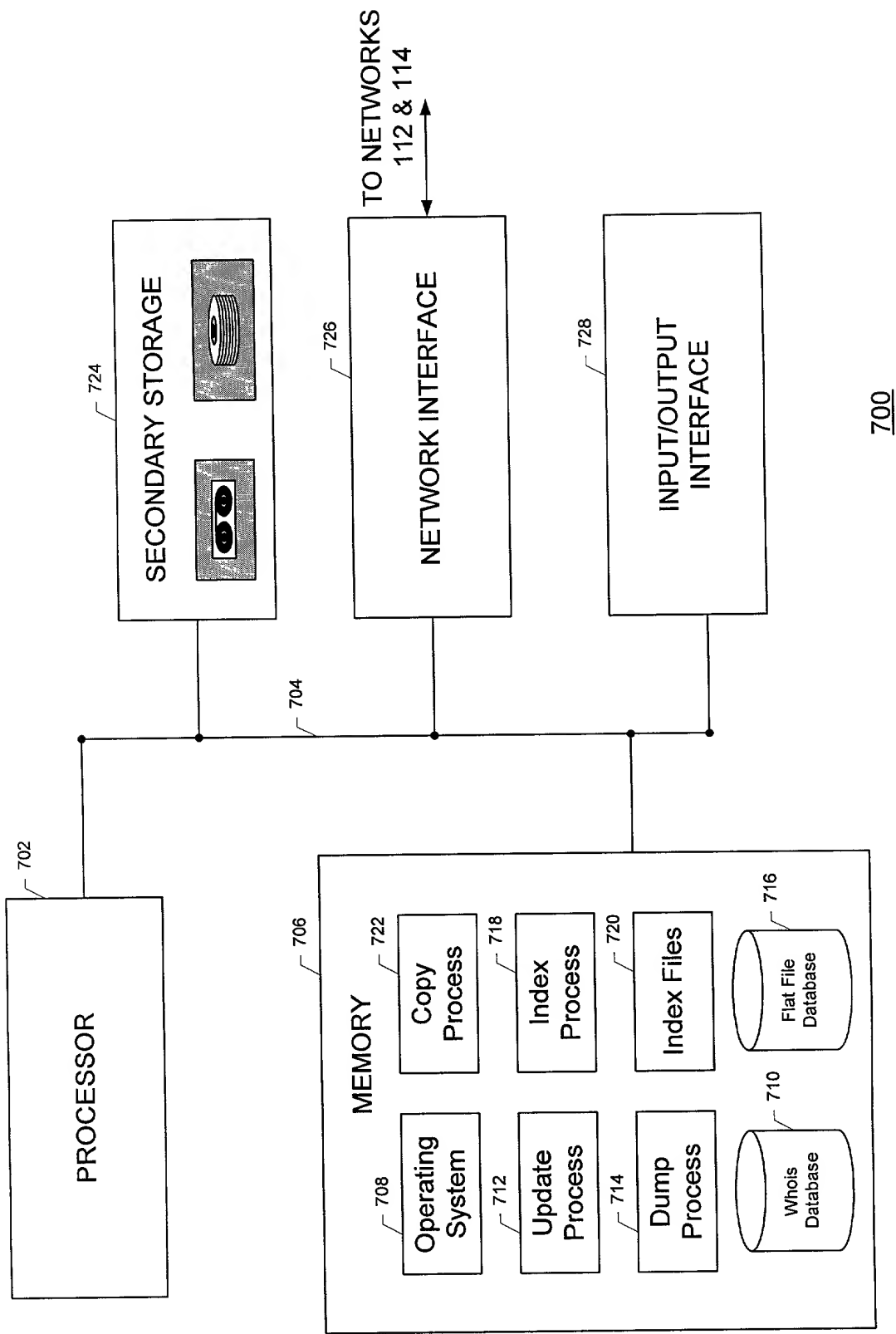
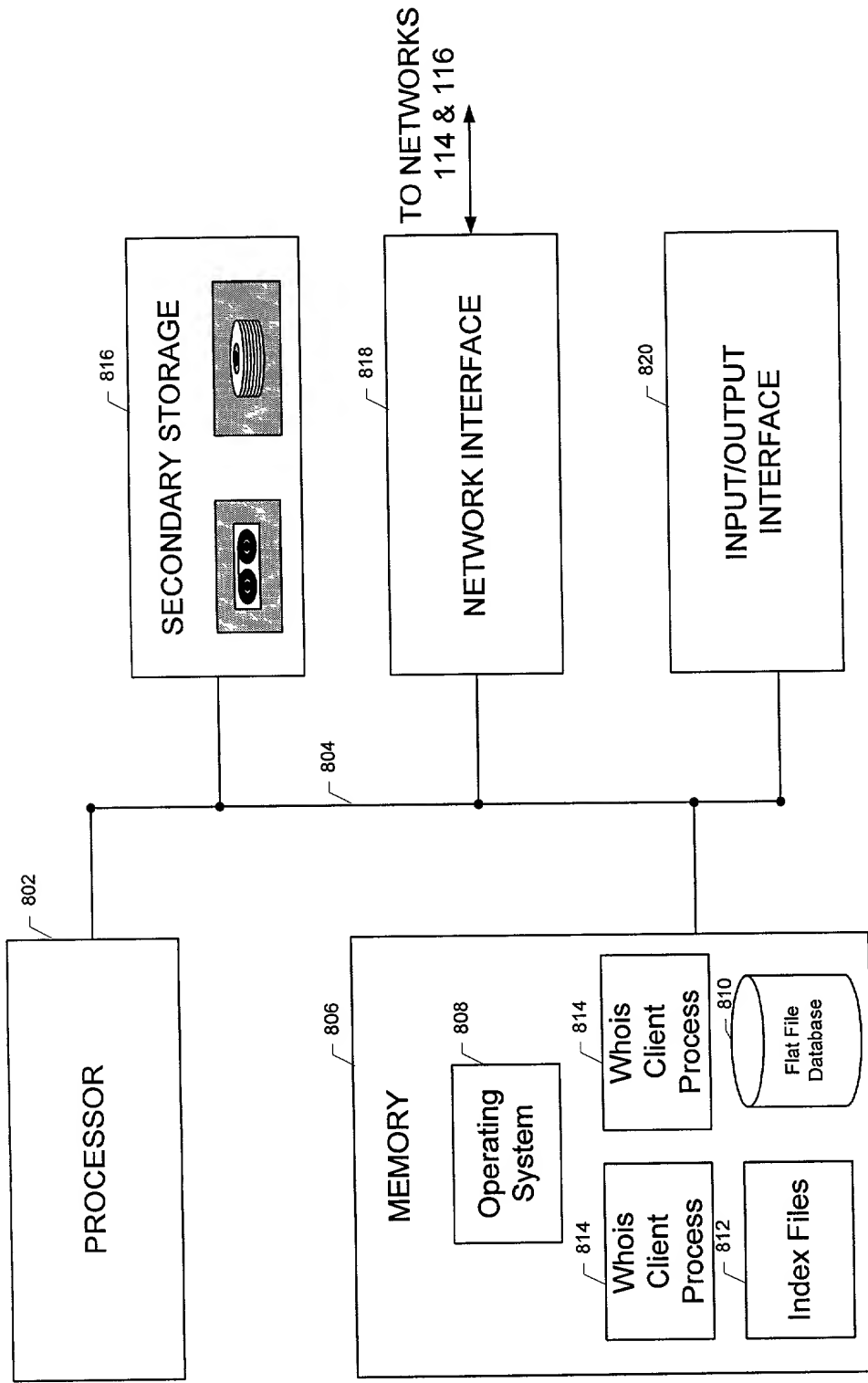


Figure 7

FIG. 8 is a block diagram of a system 800. The system 800 includes a processor 802, a memory 806, a secondary storage 816, a network interface 818, and an input/output interface 820. The processor 802 is connected to the memory 806, the secondary storage 816, the network interface 818, and the input/output interface 820. The memory 806 includes an operating system 808, two Whois Client Processes 814, index files 812, and a flat file database 810. The network interface 818 is connected to networks 114 and 116.



800

Figure 8